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APPLICANTS

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** CONTINUING DATA *****

None TT

** FOREIGN APPLICATIONS *****

None TT

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY NY	SHEETS DRAWING 8	TOTAL CLAIMS 16	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>Eugene J. Nock</i>	INITIALS TT		
Verified and Acknowledged				

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TITLE

Method and latch circuit for implementing enhanced performance with reduced quiescent power dissipation using mixed threshold CMOS devices

FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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